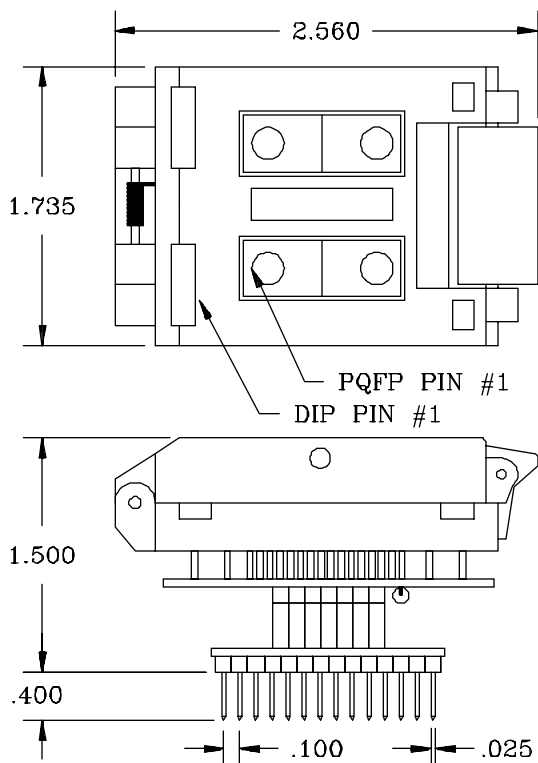**ET**®

EMULATION TECHNOLOGY, INC.

PROGRAMMING ADAPTER SPECIFICATION

Item: 1282801Q600YAM
Description: AS-128-28-01Q-6YAM

REPLACEMENT SOCKET: S-QFP-00-128-A1
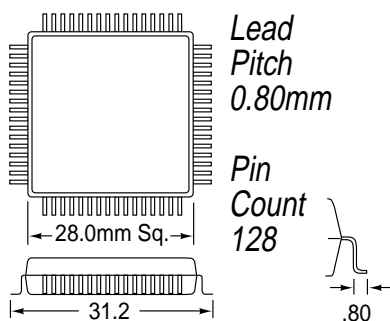**Call Emulation Technology for a replacement socket price and delivery quote.**

TYPICAL DIMENSIONS:

2.560
1.735
PQFP PIN #1
DIP PIN #1
1.500
.400
.100
.025

All Dimensions in Inches. (Assembly Drawing: F2081)

PACKAGE SPECIFICATIONS:

Lead
Pitch
0.80mm

Pin
Count
128

28.0mm Sq.
31.2
.80

All Dimensions in Millimeters.

ET Footprint Code: 128-QF13B

CONNECTION TABLE:

128-Lead PQFP SOCKET	ispLSI 1048 & ispLSI 2096* pLSI 1048 & pLSI 2096* DUT Pin Names	28-Pin DIP Base
78	SCLK	1
2 & 20	SDIN	2
3 & 46	MODE	3
4 & 18	ispEN	4
116	VIEen	5
14	VIE	6
118	VEC0	7
7	VEC1	8
8	VEC2	9
9	VEC3	10
10	VEC4	11
11	VEC5	12
12	VEC6	13
1, 17, 33, 49, 65, 81, 97, 112	GND	14
50	SDOUT	15
22	VEC7	16
45	VEC8	17
109	VEC9	18
19	RESET	19
15	Y0	20
83	Y1	21
80	Y2	22
79	Y3 & NC*	23
67	NC	24
68	NC	25
69	NC	26
70	NC	27
82 & 113	VCC	28

Notes:
1. Decoupling capacitor of 0.1uF for VCC/GND.
2. Decoupling capacitor of 0.01uF for ispEN/GND.
3. 1K ohm Resistor, ¼ W from pin 5 to pin 116.
4. 1K ohm Resistor, ¼ W from pin 6 to pin 14.
5. VECs are for pLSI.
6. All pins not listed are not connected.
7. Alternate DUT Pin names for ispLSI 2096 and pLSI 2096 are indicated with an asterisk.
8. DIP base pins are .600" wide

Compatible PQFP devices:
ispLSI 1048E and pLSI 1048E
ispLSI 2096 and pLSI 2096

Manufacturer:
Lattice

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